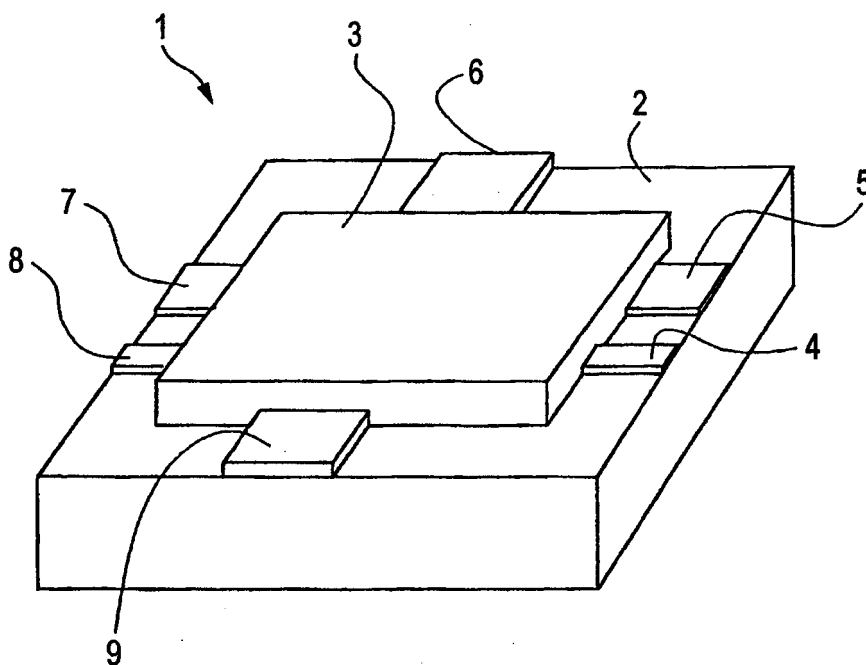


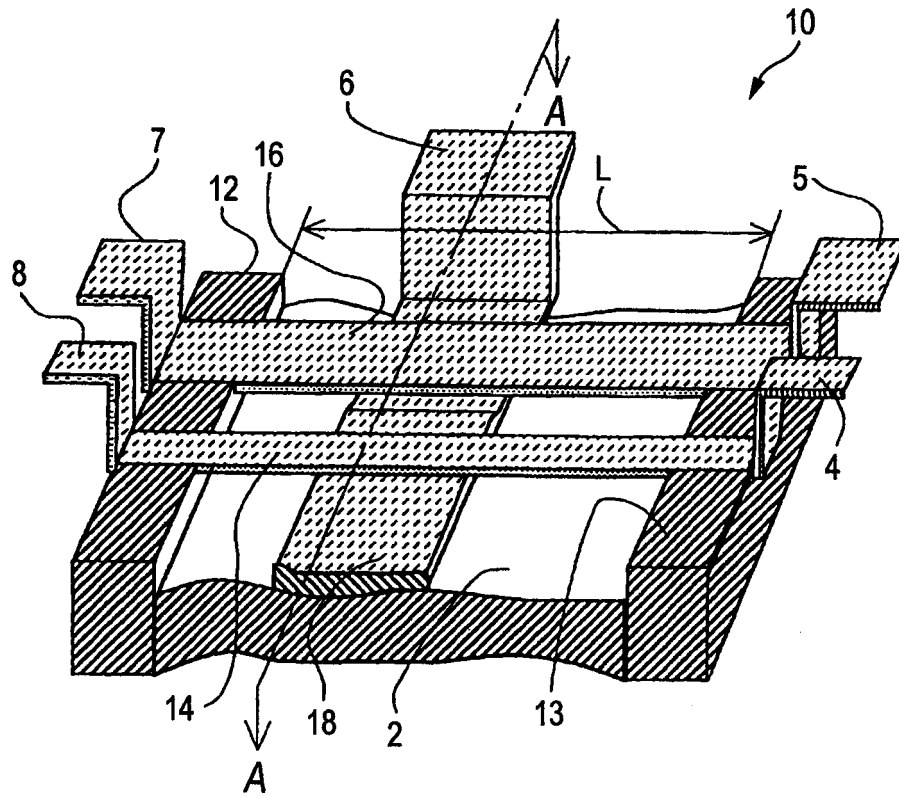
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FIG. 1



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FIG. 2



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FIG. 3

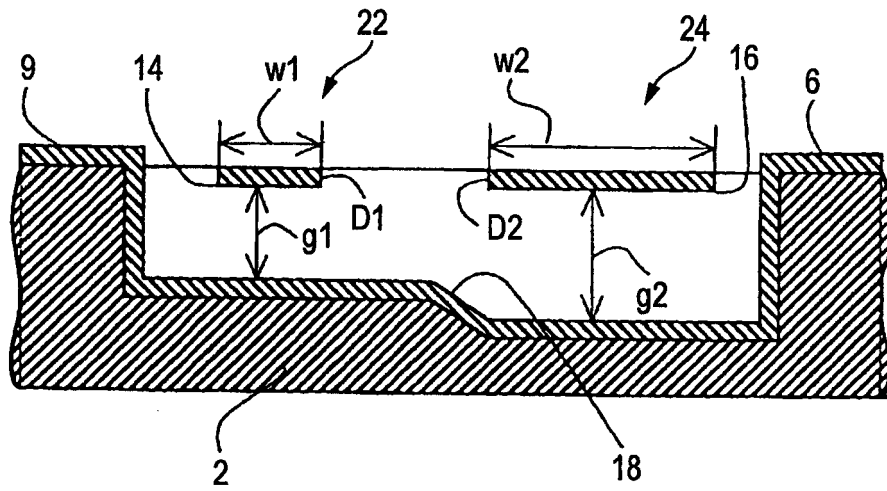
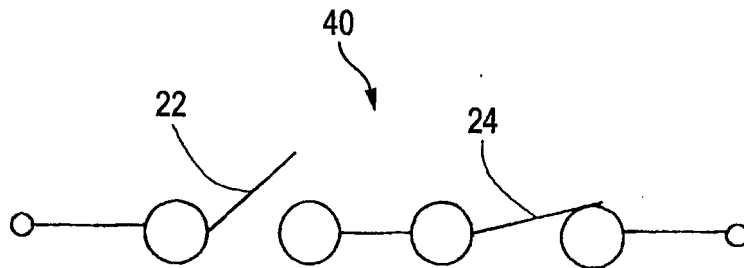
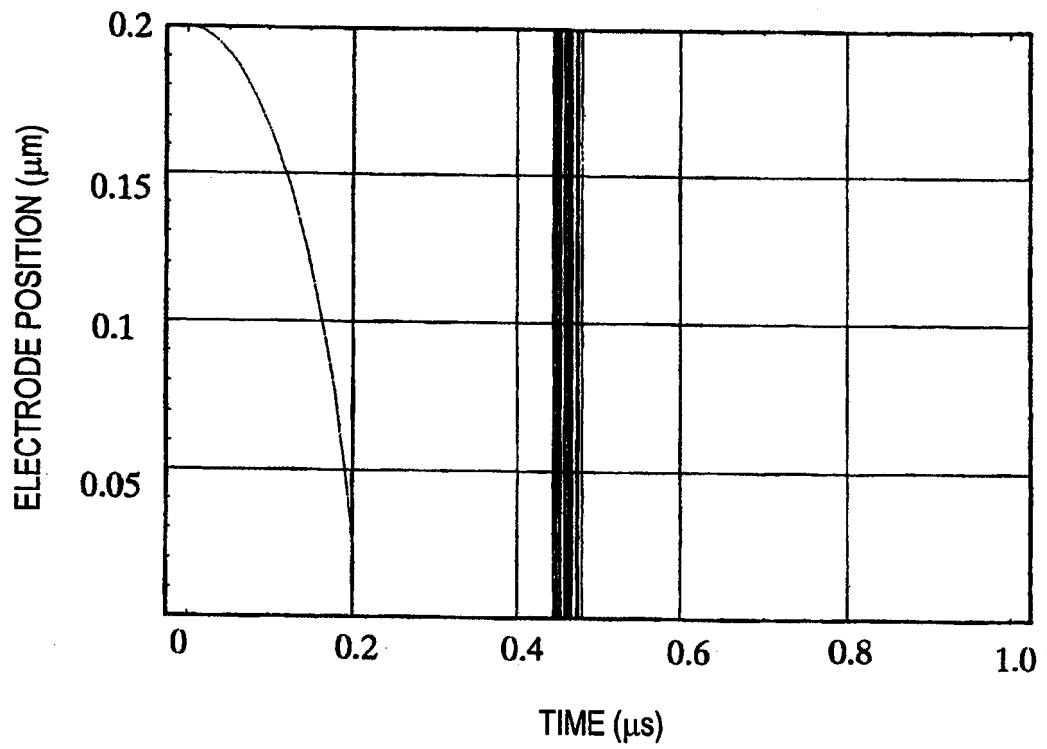


FIG. 4



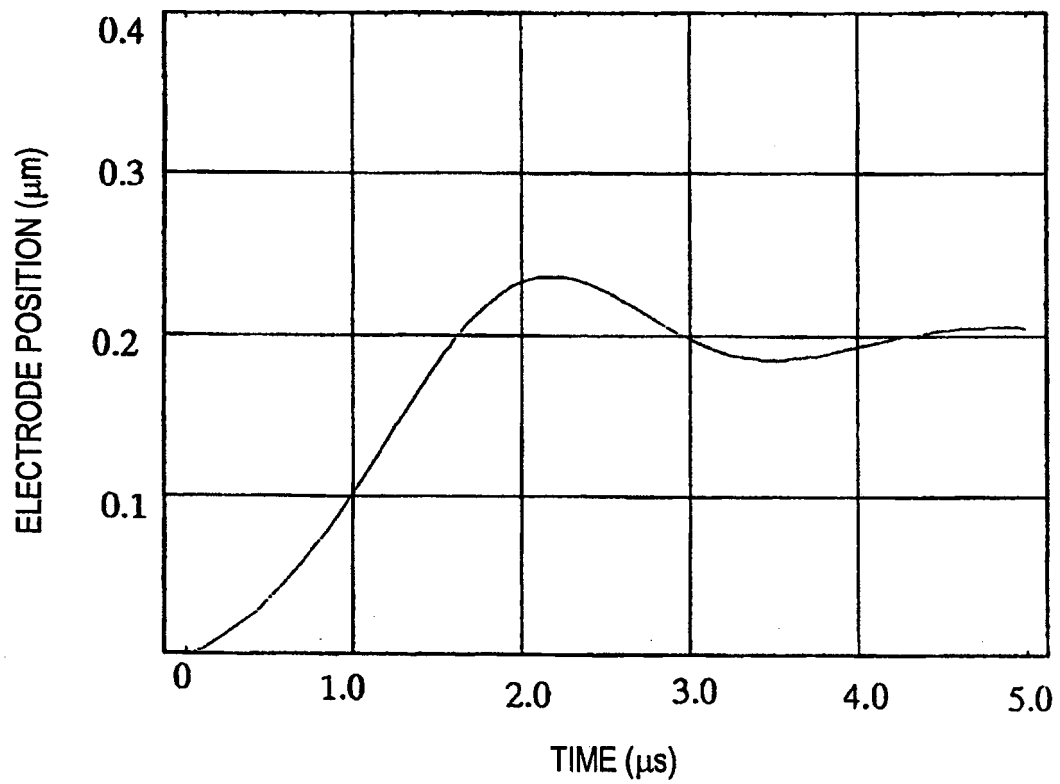
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*FIG. 5*



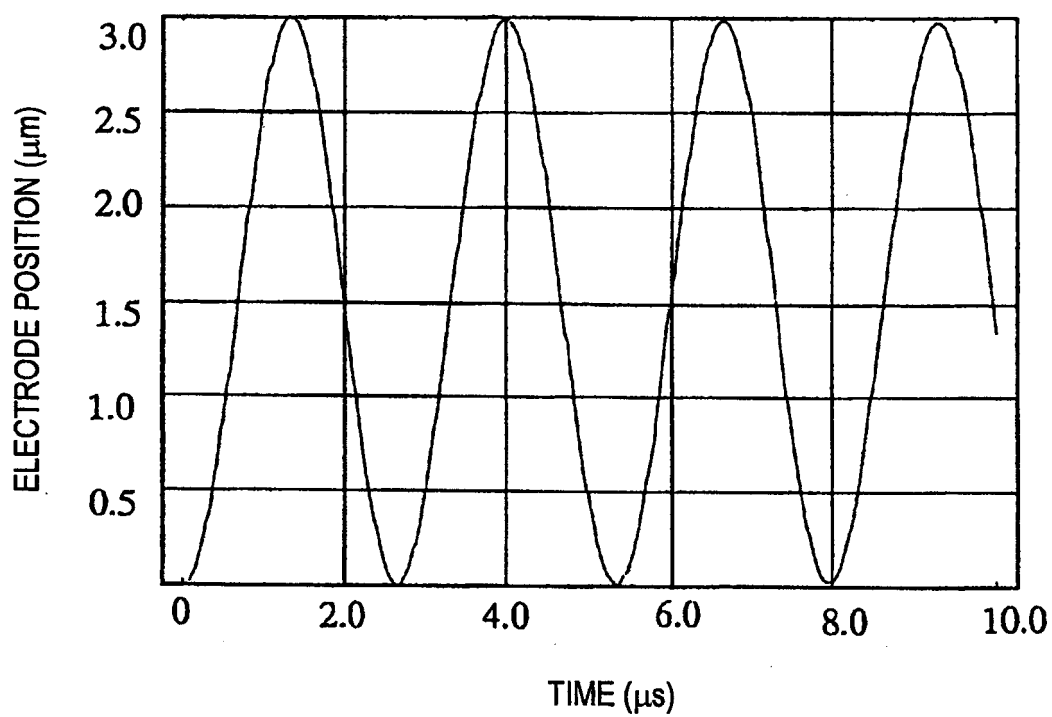
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*FIG. 6*



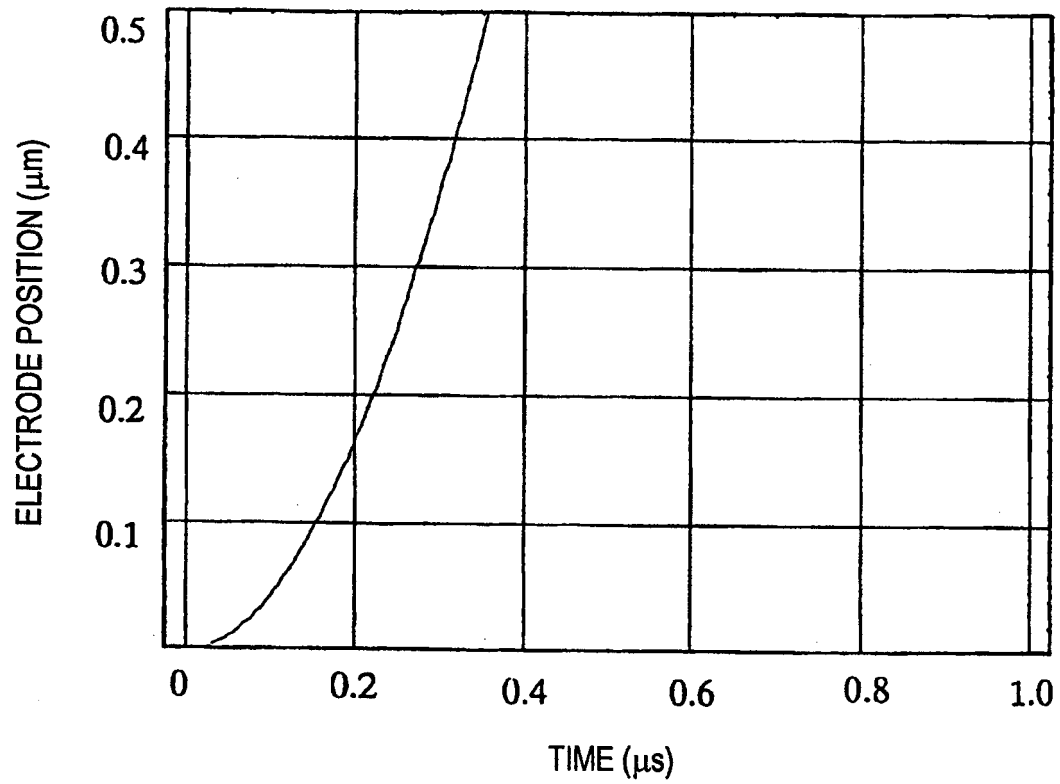
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**FIG. 7**



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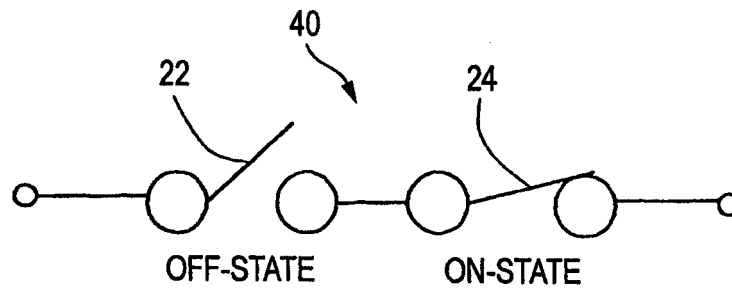
FIG. 8



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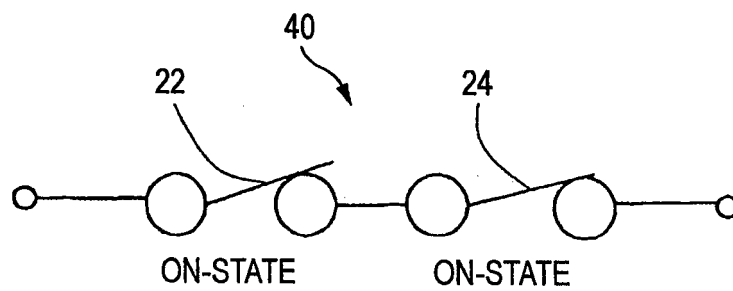
**FIG. 9 (a)**

MEMS SWITCH OFF-STATE



**FIG. 9 (b)**

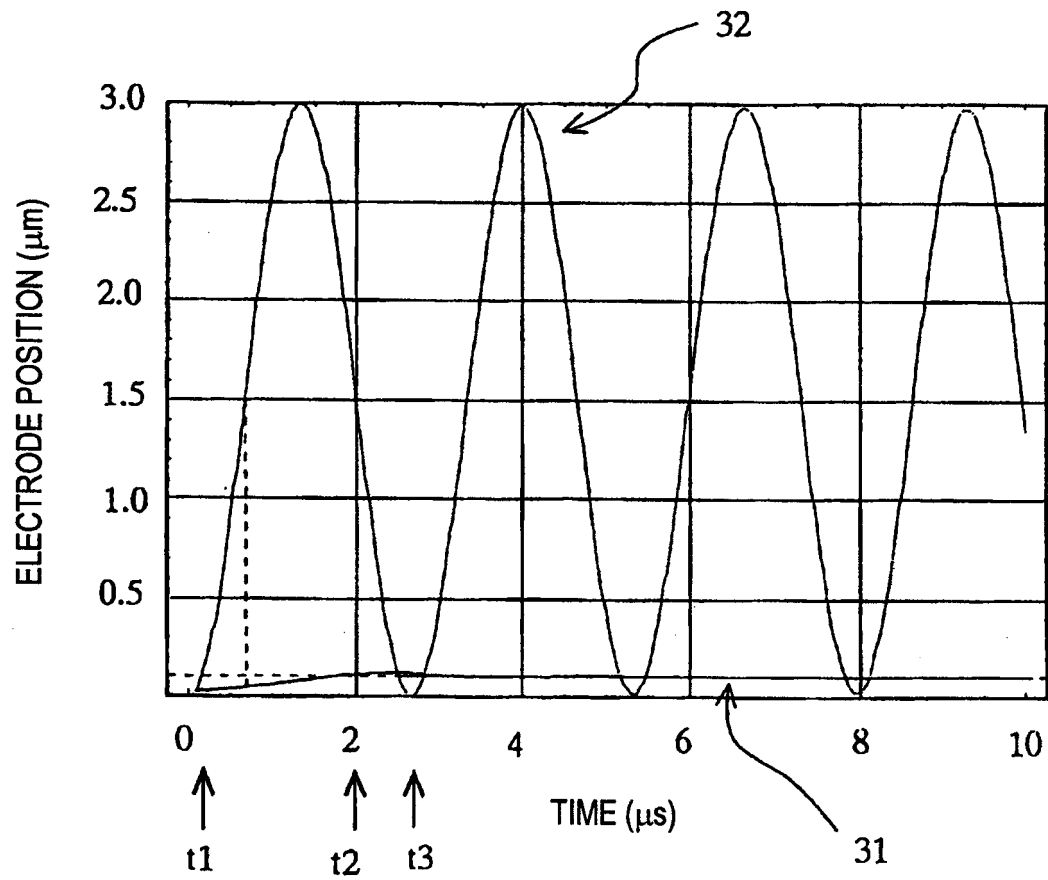
MEMS SWITCH ON-STATE



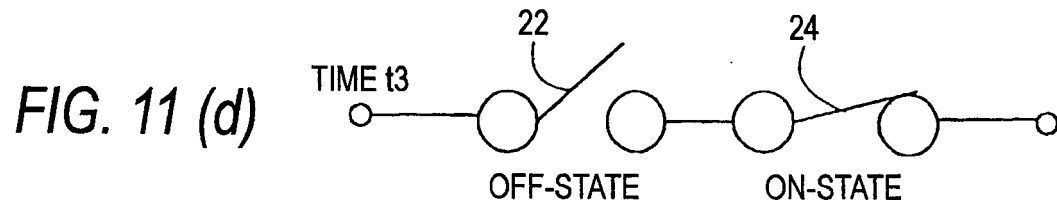
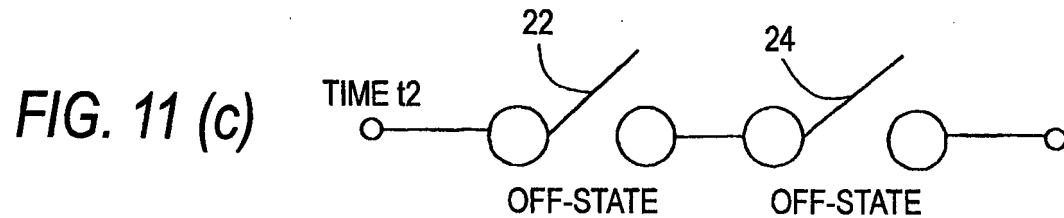
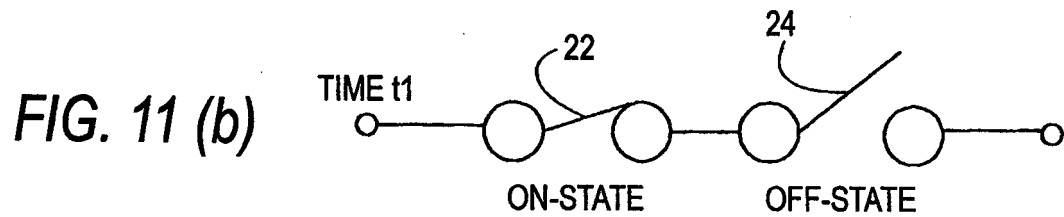
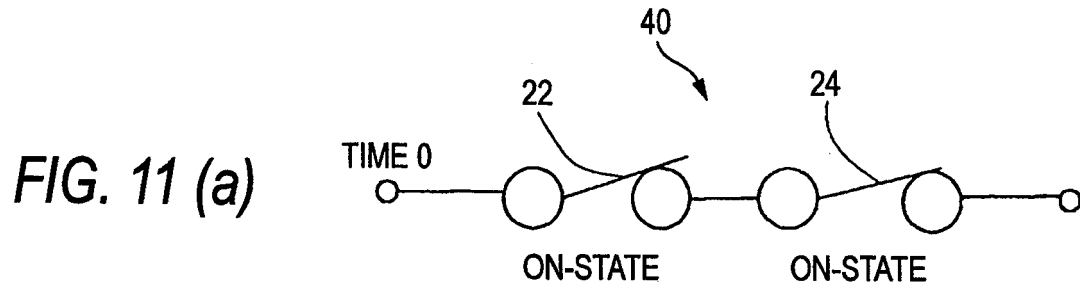


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FIG. 10

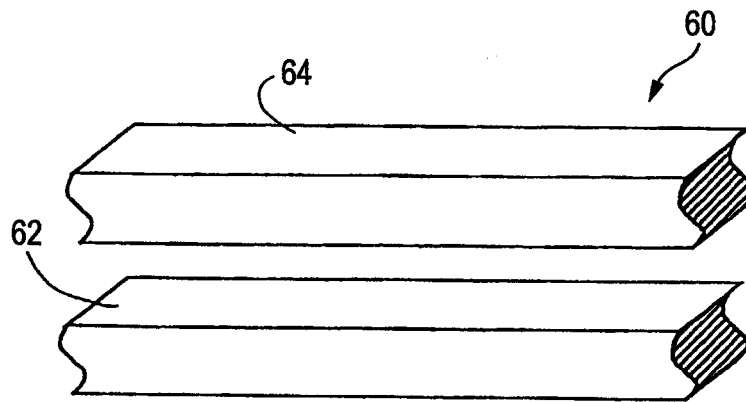


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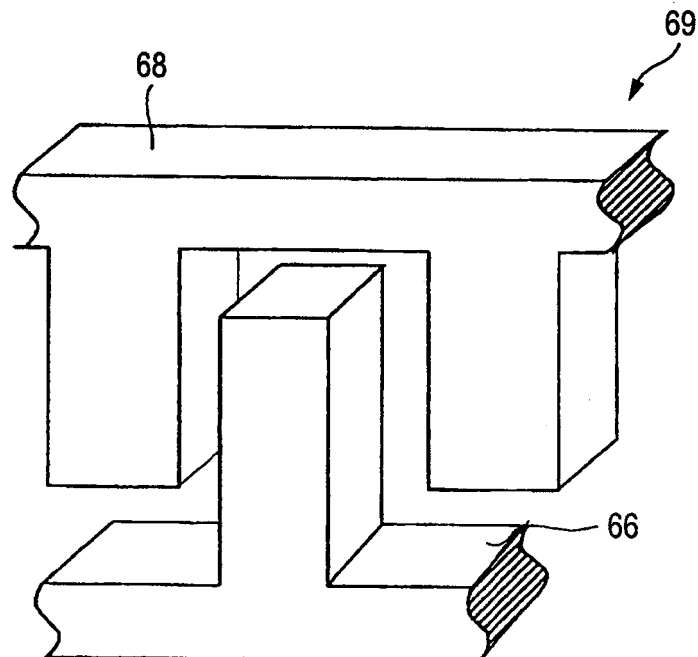


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*FIG. 12 (a)*

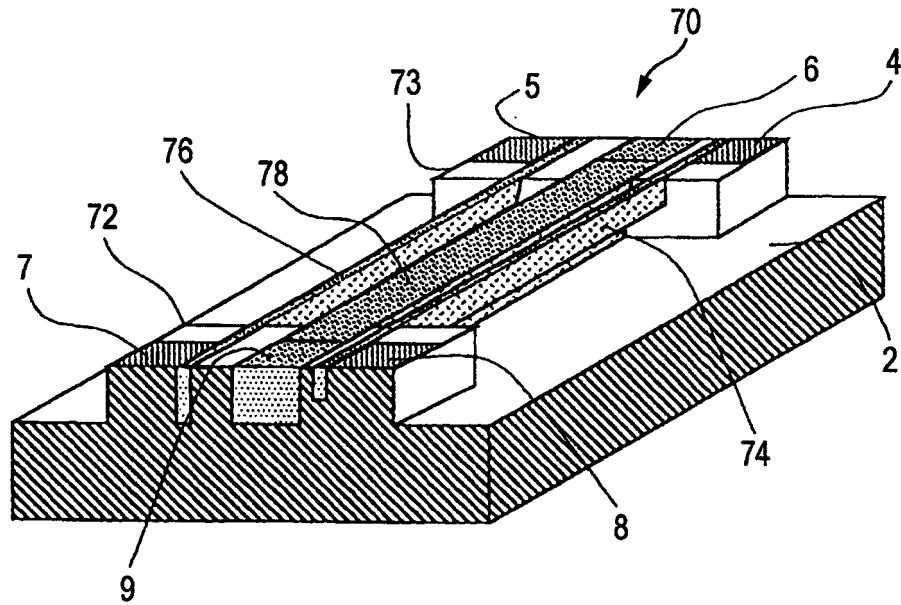


*FIG. 12 (b)*



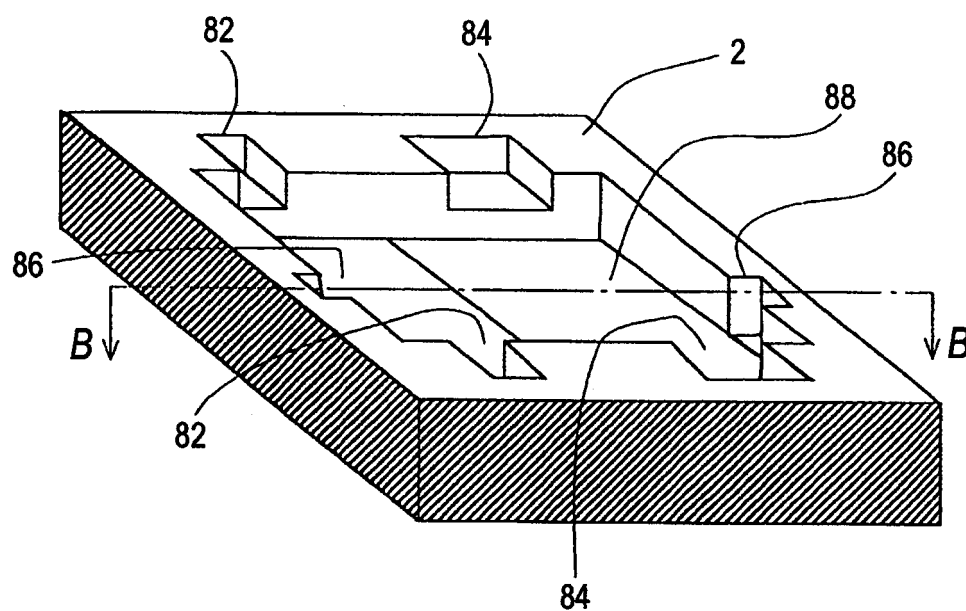
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FIG. 13



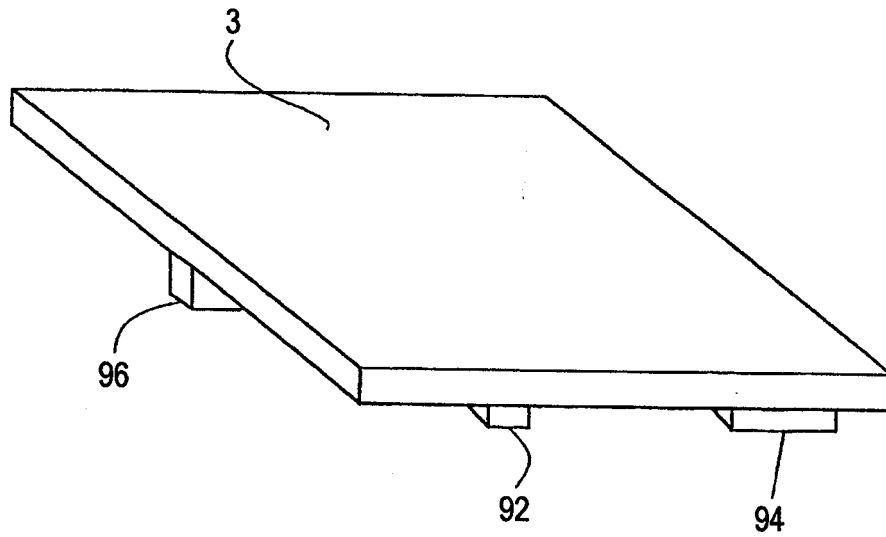
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FIG. 14



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**FIG. 15**



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FIG. 16 (a)

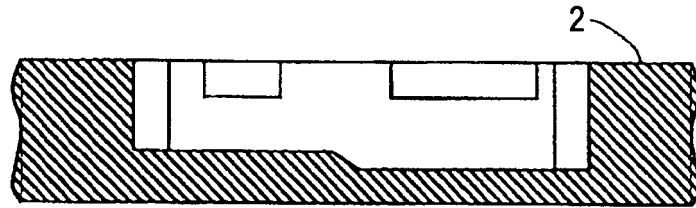


FIG. 16 (b)

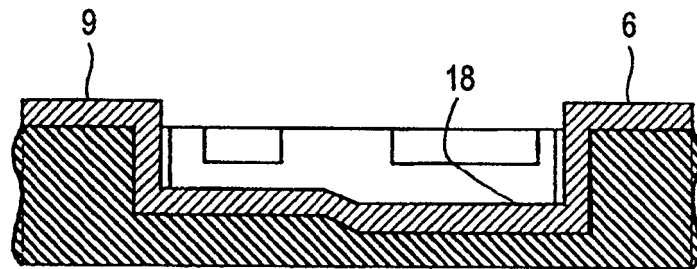


FIG. 16 (c)

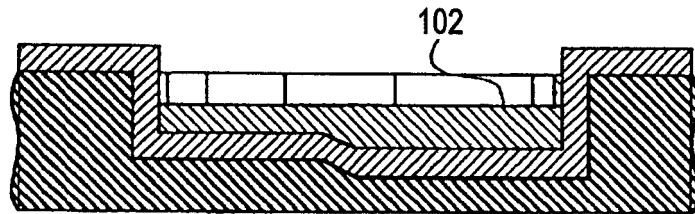


FIG. 16 (d)

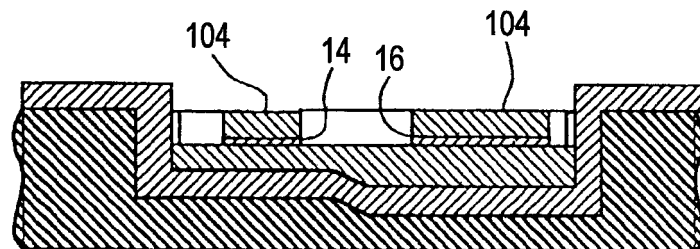
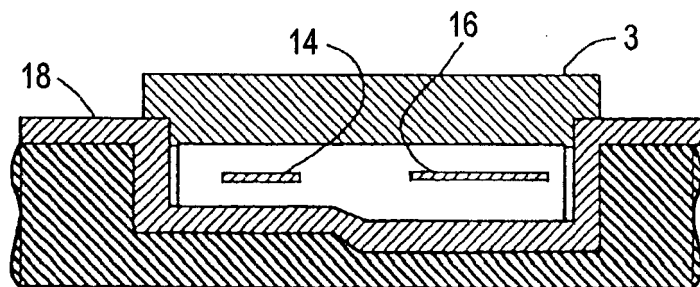
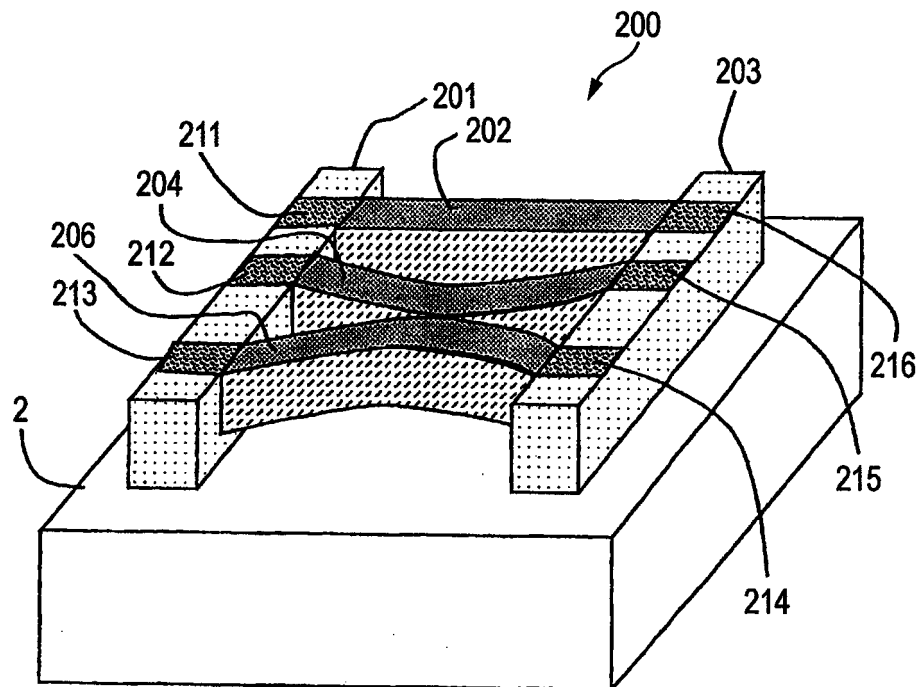


FIG. 16 (e)



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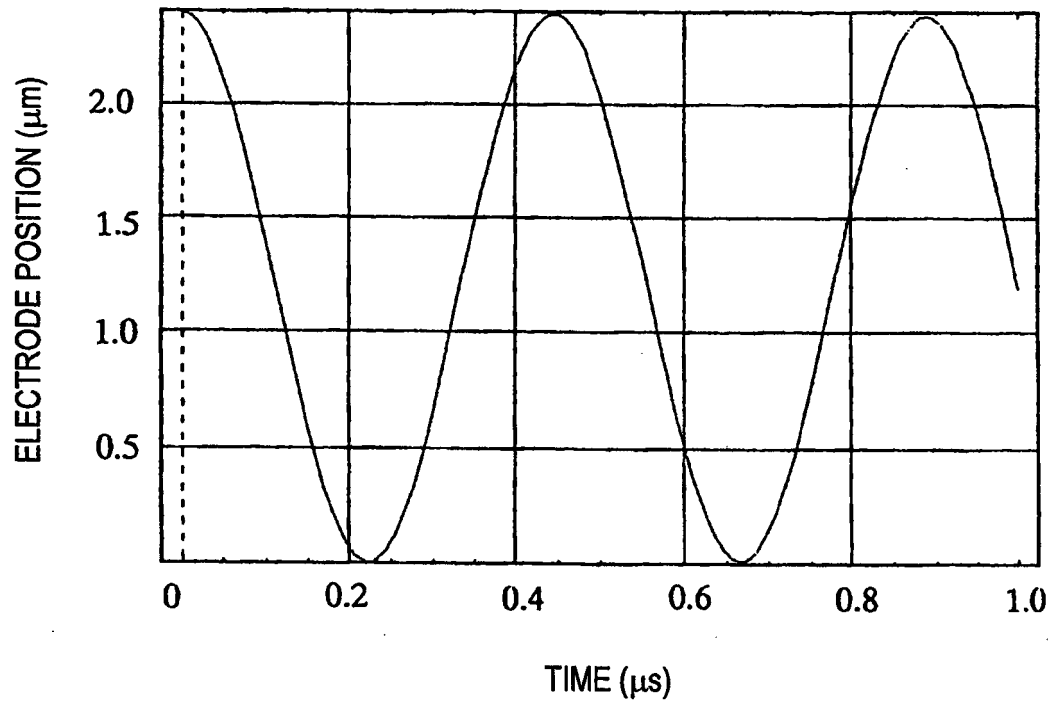
FIG. 17





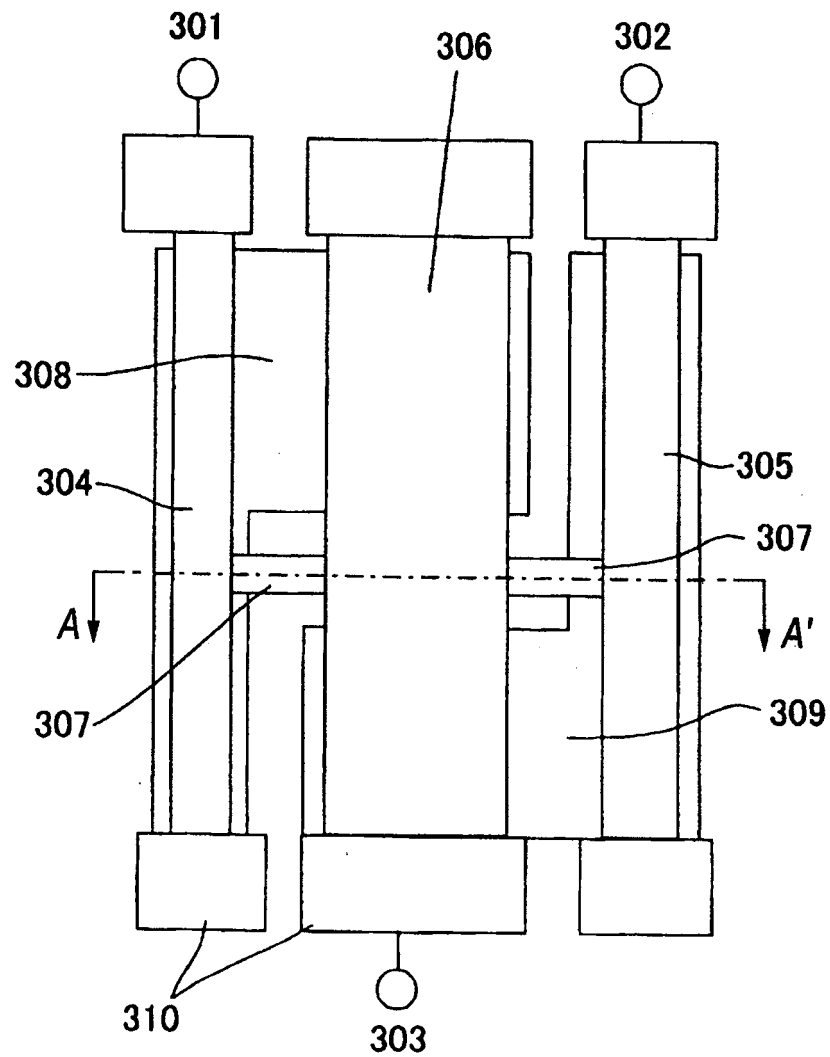
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**FIG. 18**



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**FIG. 19**



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FIG. 20 (a)

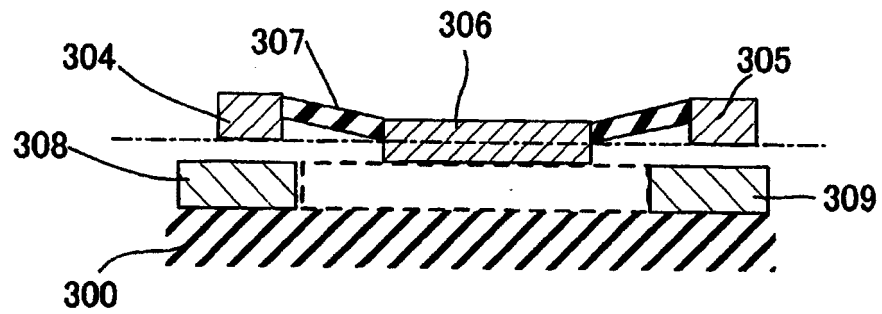


FIG. 20 (b)

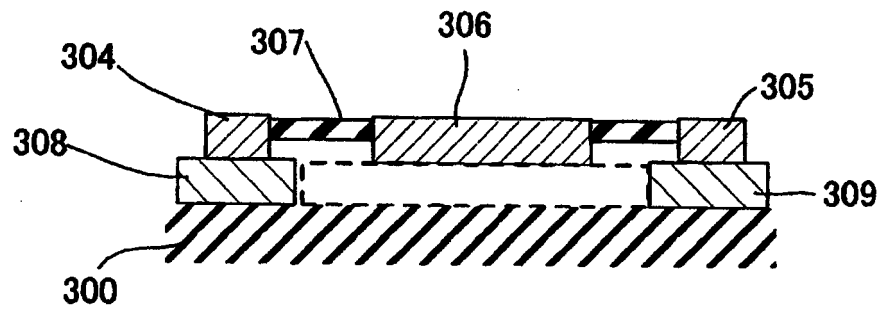


FIG. 20 (c)

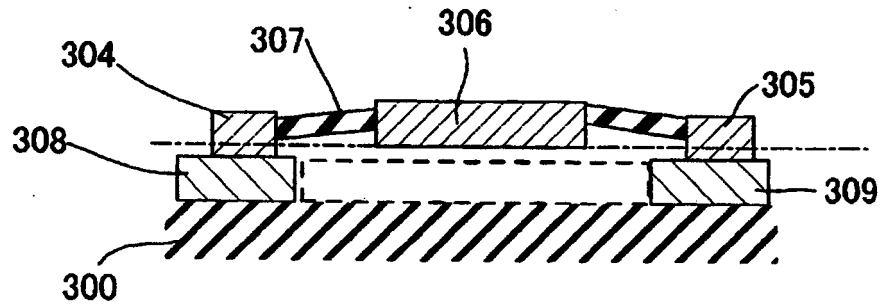


FIG. 20 (d)

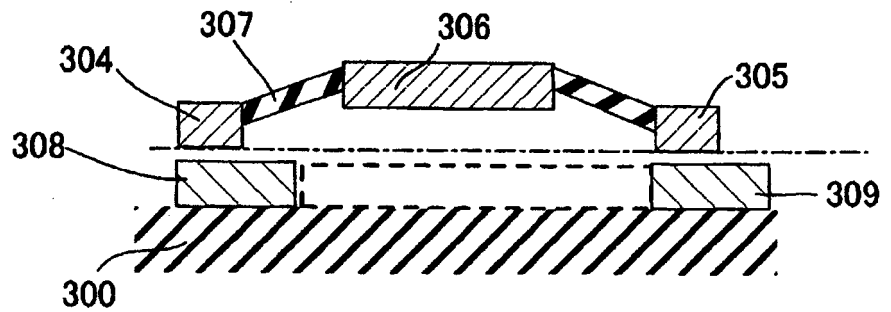
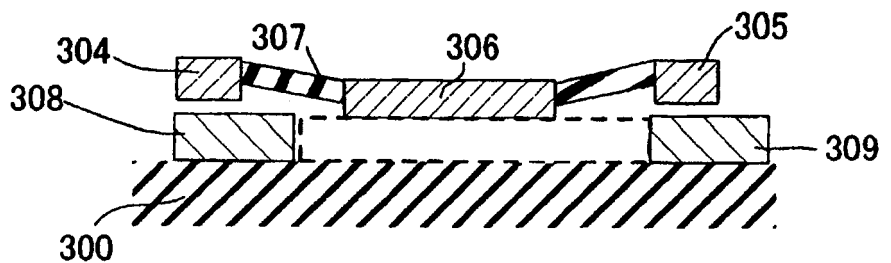


FIG. 20 (e)



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FIG. 21 (a)

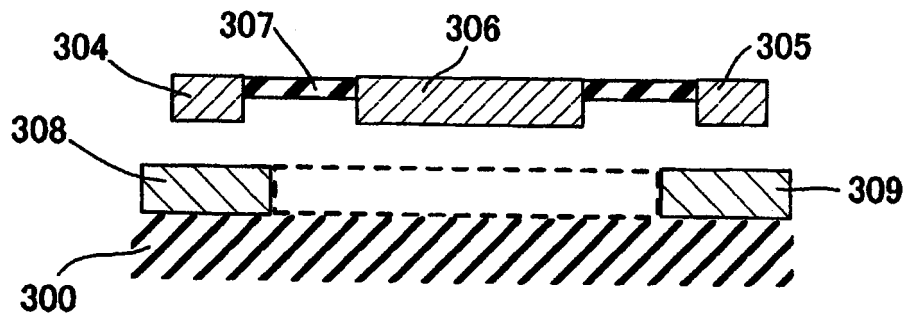


FIG. 21 (b)

